

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments	Error Definition	Errors
1	BRS	L1	1235	(pre-emphasis or preemphasis) and circuitry	US-PGPUB ; USPAT ; USOCR	2005/06/10 12:10			
2	BRS	L2	31869 62	low voltage differential signaling	US-PGPUB ; USPAT ; USOCR	2005/06/10 12:10			
3	BRS	L3	1165	(low voltage differential signaling or lvds) and L1	US-PGPUB ; USPAT ; USOCR	2005/06/10 12:12			
4	BRS	L4	38211 2	(synchronous semiconductor memory device) and (signal generating circuit) and (latch or (flip adj flop) or gate) and (transmission gate)	US-PGPUB ; USPAT ; USOCR	2005/06/10 12:13			
5	BRS	L5	713	(D adj flip adj flop) and clock and enable and "327".clas.	US-PGPUB ; USPAT ; USOCR	2005/06/10 12:14			
6	BRS	L6	41381 3	(d (latch or (flip adj flop) or gate)) and (transmission and gate) and multiplexor and shift register	US-PGPUB ; USPAT ; USOCR	2005/06/10 12:14			
7	BRS	L11	466	synchronous and semiconductor and memory and device and signal and boosting	US-PGPUB ; USPAT ; USOCR	2005/06/10 12:18			
8	BRS	L12	61550	amplifier and L6	US-PGPUB ; USPAT ; USOCR	2005/06/10 12:19			